Categorical Semantics of Cyber-Physical Systems

Christina Vasilakopoulou

University of Patras, Greece

TallCat seminar, Tallinn University of Technology

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Systems Theory and Design

Idea: provide categorical framework for modeling and analysis of systems

- Systems as boxes
- Channels of info flow as wires
- Inhabitants

Analyse the composite system using the analyses of the particular system components and their specific wired interconnection.

► System architecture and behavior in single model...
Outline

1. The monoidal category of labelled boxes and wiring diagrams
2. Systems as algebras for wiring diagrams
3. Interval sheaves
4. Abstract machines
Labelled boxes and wiring diagrams

There is a category $\mathbf{WD}$ that consists of the following:

- objects are pairs of sets $X = (X_{\text{in}}, X_{\text{out}})$

Example: an object $(\mathbb{R} \times \mathbb{N}, \{\top, \bot\})$ is an empty box

A process that can later populate the box is a function

$$f(r, n) = \begin{cases} 
\top & \text{if } r = n \\
\bot & \text{if } r \neq n 
\end{cases}$$
• morphisms \((X_{\text{in}}, X_{\text{out}}) \rightarrow (Y_{\text{in}}, Y_{\text{out}})\) are pairs of ‘special’ functions
\[
(X_{\text{out}} \times Y_{\text{in}} \xrightarrow{\phi_{\text{in}}} X_{\text{in}}, X_{\text{out}} \xrightarrow{\phi_{\text{out}}} Y_{\text{out}})
\]

Example: the morphism \((\mathbb{R}^2 \times \mathbb{N}, \{\top, \bot\} \times \mathbb{N}) \rightarrow (\mathbb{R}, \{\top, \bot\})\) as in

\[
\begin{align*}
\phi_{\text{in}} &: \{\top, \bot\} \times \mathbb{N} \times \mathbb{R} \rightarrow \mathbb{R}^2 \times \mathbb{N} \\
\phi_{\text{out}} &: \{\top, \bot\} \times \mathbb{N} \rightarrow \{\top, \bot\} \\
\end{align*}
\]

is described by the two functions

\[
\begin{align*}
\phi_{\text{in}}(x, n, r) &= (r, r, n) \\
\phi_{\text{out}}(x, n) &= x
\end{align*}
\]
Monoidal structure

- tensor product $X \otimes Y = (X_{in} \times Y_{in}, X_{out} \times Y_{out})$

Example: for three boxes $(\mathbb{R} \times \mathbb{N}, \{\top, \bot\})$, $(\mathbb{C}, \mathbb{R})$ and $(\mathbb{C}, \mathbb{C}^3)$, their tensor is $(\mathbb{R} \times \mathbb{N} \times \mathbb{C}^2, \{\top, \bot\} \times \mathbb{R} \times \mathbb{C}^3)$

With appropriate composition law and identities, all axioms hold.

The category $\mathbf{WD}$ of labeled boxes and wiring diagrams is a monoidal category.
Any wiring interconnection can be expressed as a morphism in \( WD \):

\[
\begin{align*}
\phi_{\text{in}} : \mathbb{R} \times \{\top, \bot\} \times C \times N &\rightarrow C \times \mathbb{R} \times N, \\
(r, x, c, n) &\mapsto (c, r, n)
\end{align*}
\]

\[
\phi_{\text{out}} : \mathbb{R} \times \{\top, \bot\} &\rightarrow \{\top, \bot\}, \\
(r, x) &\mapsto x
\]

All functions are made up from projections, duplications and switches: \( \text{Set} \) could be replaced by \( \text{any} \) typing category \( C \) with finite products.
The operad of wiring diagrams

Before, first ‘aligned’ all boxes to form their tensor and then computed the wiring; this relates to the *underlying operad* of any monoidal category.

A *colored operad* or *multicategory* $\mathcal{P}$ consists of

- a set of objects (colors)
- for each $(n + 1)$-tuple of objects, a set of $n$-ary operations $\mathcal{P}(c_1, \ldots, c_n; c)$
- an identity operation $\text{id}_c \in \mathcal{P}(c; c)$
- a composition formula for nesting of operations

subject to associativity and unitality axioms.

▶ Every monoidal category $\mathcal{V}$ gives rise to an operad $\mathcal{O}(\mathcal{V})$ with same objects and $n$-ary operations $\mathcal{O}(\mathcal{V})(c_1, \ldots, c_n; c) := \mathcal{V}(c_1 \otimes \ldots \otimes c_n, c)$.

★ Pictures are nicer in the operad $\mathcal{O}(\text{WD})$ than WD!
Lax monoidal functors $F : (\mathcal{V}, \otimes, 1) \to (\text{Cat}, \times, 1)$ are called $\mathcal{V}$-algebras.

Fully faithful underlying operad functor $\text{SMonCat}_\ell \xrightarrow{\mathcal{O}} \text{SOpd}$ induces

$$\mathcal{V}\text{-Alg} \cong \text{SOpd}(\mathcal{O}\mathcal{V}, \mathcal{O}\text{Cat}) =: (\mathcal{O}\mathcal{V})\text{-Alg}.$$ 

$F : \text{WD} \xrightarrow{\text{FX}} \text{Cat}$ gives semantics to boxes, composite operation to wiring diagrams and parallelizing operation to subsystems via $FX \times FY \to F(X \otimes Y)$.
**Algebra of contracts**

There is a lax monoidal functor \( \text{Cntr}: \text{WD} \rightarrow \text{Cat} \) that

- to each box \( X_{in} \xrightarrow{\bullet} X_{out} \) assigns category of *contracts*, i.e. relations
  \[
  R \subseteq X_{in} \times X_{out}
  \]

- to each wiring diagram \( X \xrightarrow{\circlearrowleft} Y \) assigns formula
  \[
  P \xrightarrow{\phi} R_{in} \times X_{out} \xrightarrow{(\phi_{in}, \pi_2)} X_{in} \times X_{out}
  \]

that, given contracts on subsystems, produces contract on composite.

- For each \( R_X \subseteq X_{in} \times X_{out} \) and \( R_Y \subseteq Y_{in} \times Y_{out} \) assigns contract
  \[
  R_X \times R_Y \subseteq X_{in} \times X_{out} \times Y_{in} \times Y_{out} \cong X_{in} \times Y_{in} \times X_{out} \times Y_{out}
  \]

\[
R_X = [4, 5] \times [4, 5], \quad R_Y = [8, 9] \times [8, 9]
\]
\[
R_Z = [3, 5] \times [7, 9] \times \mathbb{R} \times [0, 1] \text{ produce } \]
\[
R_A = [4, 5] \times [8, 9] \times \mathbb{R} \times [0, 1]
\]
Algebra of discrete dynamical systems

Fix two sets $X_{\text{in}}, X_{\text{out}}$. A DDS (or Moore machine) consists of a set of states $S$ along with two functions, $\text{upd}: X_{\text{in}} \times S \to S$ that updates the state given some input, and $\text{rdt}: S \to X_{\text{out}}$ that readouts an output value.

![Diagram of a DDS]

$(S = \{s_1, s_2\}, \text{upd}, \text{rdt})$ is the NOT machine

Model discrete systems as a WD-algebra:

$$\begin{align*}
\text{WD} & \rightarrow \text{Cat} \\
X = (X_{\text{in}}, X_{\text{out}}) & \rightarrow \text{DDS}(X_{\text{in}}, X_{\text{out}}) \\
\phi & \downarrow \\
Y = (Y_{\text{in}}, Y_{\text{out}}) & \rightarrow \text{DDS}(Y_{\text{in}}, Y_{\text{out}})
\end{align*}$$

via $(S, f^{\text{upd}}, f^{\text{rdt}}) \mapsto (S, g^{\text{upd}}, g^{\text{rdt}})$,

$$
\begin{align*}
g^{\text{upd}}(y, s) &= f^{\text{upd}}(\phi_{\text{in}}(y, f^{\text{rdt}}(s)), s) \\
g^{\text{rdt}}(s) &= \phi_{\text{out}}(f^{\text{rdt}}(s))
\end{align*}
$$
Systems theory in categorical terms

▶ The contracts algebra corresponds to the *requirements* part of systems analysis and design, which is fundamental for safety and control.

▶ Other WD-algebras, like the discrete or continuous dynamical systems, correspond to the *behavior* part, i.e. the physical specification of the process that inhabits the boxes.

▶ The categorical syntax of labeled boxes and wirings corresponds to the *architecture* part. Importantly, choosing subcomponents $X_1, \ldots, X_n$ of a system $Y$ as well as the way they are wired together (share information) is choosing a morphism $\phi: X_1 \otimes \ldots \otimes X_n \to Y$ in WD, i.e. an object in the slice category $\text{WD}/Y$.

\[
\begin{array}{ccc}
A & \xrightarrow{f} & B \\
\downarrow{\phi} & & \downarrow{\psi} \\
Y & & \end{array}
\]

architectural choices and their relation
Case study: UAV

★ Using the sub-algebra of linear time-invariant systems, can analyze the behavior of an unmanned aerial vehicle and also decompose it to a system architecture and constraint it via contracts.

\[
I_1 \otimes I_2 \otimes P_1 \otimes P_2 \otimes V \otimes X \otimes Y \otimes Z \otimes U \otimes F \xrightarrow{f \otimes g \otimes h} L \otimes C \otimes D \xrightarrow{k} UAV
\]
Modeling Time: Categories of intervals

\( \mathbb{R}_{\geq 0} \) positive reals, \( \text{Tr}_p : \mathbb{R}_{\geq 0} \rightarrow \mathbb{R}_{\geq 0} \) translation-by-\( p \).

- Category \textbf{Int} of continuous intervals has objects \( \mathbb{R}_{\geq 0} \), morphisms

  \[ \text{Int}(\ell, \ell') = \{ \text{Tr}_p | p \in \mathbb{R}_{\geq 0} \text{ and } p \leq \ell' - \ell \} \]; equivalently via image

  \[ \begin{array}{c}
  [0, \ell] \subseteq [0, \ell'] \\
  0 \\
  p \\
  p + \ell \\
  \ell'
  \end{array} \]

- Category \textbf{Int}_N of discrete intervals, \( \text{ob} = \mathbb{N} \), \( n \xrightarrow{\text{Tr}_p} n' \) by \( p \in \mathbb{N} \).

\[ \star \text{ Int-presheaves: for } A : \textbf{Int}^{\text{op}} \rightarrow \textbf{Set}, \text{ view section } x \in A(\ell') \& \text{ restriction } A(\text{Tr}_p)(x) \]
Sheaves on intervals

For $\ell \in \mathbf{Int}$ and $0 \leq p \leq \ell$, the pairs $p \xrightarrow{[0,p]} \ell$, $(\ell-p) \xrightarrow{[p,\ell]} \ell$ form a cover for $\ell$. These generate a coverage for $\mathbf{Int}$; similarly for $\mathbf{Int}_N$.

$\hat{\mathbf{Int}}$ and $\hat{\mathbf{Int}}_N$ are the toposes of continuous and discrete interval sheaves, i.e. $\mathbf{Int}_{(N)}$-presheaves whose compatible sections glue.

Idea: $\hat{\mathbf{Int}}_{(N)}$-labeled boxes have ports carrying very general time-based signals, expressed as sheaves of ‘all possible behaviors’.

Examples

- $\hat{\mathbf{Int}}_N \cong \mathbf{Grph}$, so every graph gives a discrete interval sheaf
- $L : \mathbf{Set} \to \hat{\mathbf{Int}}_N$ by $L(X)(n) = X^{n+1}$, non-empty $X$-lists sheaf
- $F : \mathbf{Set} \to \hat{\mathbf{Int}}$ by $F(X)(\ell) = \{ f : [0, \ell] \to X \}$, sheaf of functions
- $\text{Ext}_\epsilon : \hat{\mathbf{Int}} \to \hat{\mathbf{Int}}$ by $\text{Ext}_\epsilon(A)(\ell) = A(\ell + \epsilon)$, $\epsilon$-extension sheaf
Abstract machines

Purpose: define abstract systems in terms of $\text{Int}$-sheaves; perceive known dynamical systems as special cases; coherently interconnect arbitrary systems and study their behavior on common ground.

A continuous machine with input & output $A & B \in \tilde{\text{Int}}$ is

\[
\begin{array}{ccc}
  & S & \\
p^i & \rightarrow & p^o \\
A & \rightarrow & B \\
\end{array}
\]

\[S - \text{state sheaf} \]
\[p^i - \text{input sheaf map} \]
\[p^o - \text{output sheaf map} \]

\[\text{Mch}(A, B) = \tilde{\text{Int}}/_{A \times B} \text{ the topos of continuous } (A, B)\text{-machines.} \]

For $A, B \in \tilde{\text{Int}}_N$, discrete machines $\text{Mch}_N(A, B) = \tilde{\text{Int}}_N/_{A \times B}$.  

\[
\text{Mch}(A, B) = \tilde{\text{Int}}/_{A \times B} \text{ the topos of continuous } (A, B)\text{-machines.} 
\]
Continuous machines form a $\mathbf{WD}_{\text{Int}}$-algebra

Functor $\text{Mch}: \mathbf{WD}_{\text{Int}} \to \mathbf{Cat}$ by $(X_{\text{in}}, X_{\text{out}}) \mapsto \text{Mch}(X_{\text{in}}, X_{\text{out}})$ and

$$
\begin{array}{ccc}
S & \mapsto & S \\
(p^i, p^o) & \text{Mch}(\phi) & \\
\downarrow & & \downarrow \\
X_{\text{in}} \times X_{\text{out}} & & \\
\end{array}
$$

$$
\begin{array}{ccc}
T & \mapsto & S \\
(q^i, q^o) & \downarrow & \\
\downarrow & & \downarrow \\
Y_{\text{in}} \times X_{\text{out}} & \rightarrow & X_{\text{in}} \times X_{\text{out}} \\
1 \times \phi_{\text{out}} & \downarrow & \\
\downarrow & & \\
Y_{\text{in}} \times Y_{\text{out}} & & \\
\end{array}
$$

In fact, for any $\mathcal{C}$ with pullbacks, this process is

$$
\mathcal{C}/X_{\text{in}} \times X_{\text{out}} \xrightarrow{(\phi_{\text{in}}, \pi_2)^*} \mathcal{C}/Y_{\text{in}} \times X_{\text{out}} \xrightarrow{(1 \times \phi_{\text{out}})!} \mathcal{C}/Y_{\text{in}} \times Y_{\text{out}}.
$$

Finally, lax monoidal structure by taking products of spans:

$$
(S \xrightarrow{(p^i, p^o)} X_{\text{in}} \times X_{\text{out}}, T \xrightarrow{(q^i, q^o)} Z_{\text{in}} \times Z_{\text{out}}) \mapsto (p^i \times q^i, p^o \times q^o)
$$
Total and deterministic machines

Characteristics of interest: for initial state and input, the machine

- uniquely evolves or ‘stays idle’ $\Rightarrow$ determinism
- always evolves $\Rightarrow$ totality

▶ Continuous machines $\begin{array}{c} A \\ S \\ B \end{array}$ are neither in general:

Starting in state germ $s_0$, for input $a$ over $\ell$-interval, there may or may not be $s_0$-extension

★ A total machine would have at least one extension, whereas a deterministic machine would have maximum one extension.
There exist subalgebras of $\text{Mch}_N$: $\text{WD}_{\text{Int}} \to \text{Cat}$ of total and deterministic machines, by imposing conditions on $p^i$ and $q^i$.

There are algebra maps from discrete dynamical systems

and from continuous dynamical systems

Algebra maps ‘translate’ between various processes; can then interconnect arbitrary systems & study them on common ground.
Thank you for your attention!